

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 2

Complete if Known

Application Number	10/003,951
Filing Date	10/30/2001
First Named Inventor	Frederic Reblewski
Art Unit	2125
Examiner Name	Paladini, Albert William
Attorney Docket Number	003921.00069

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AWP		US- 5,663,900	09-1997	Bhandari et al.	
AWP		US- 5,680,583	10-21-1997	Kuijsten	
AWP		US- 5,821,773	10-13-1998	Norman et al.	
AWP		US- 5,940,603	08-1999	Huang, Thomas B.	
AWP		US- 5,999,725	12-1999	Barbier et al.	
AWP		US- 6,184,707	02-06-2001	Norman et al.	
AWP		US- 2002/0055828	05-2002	Swoboda et al.	
AWP		US- 2002/0118168	08-2002	Kim, Mahn Bok	
AWP		US- 6,496,913	12-2002	DeHon et al.	
AWP		US- 2002003/0182641	09-2003	Vang, Sei-Yang	
AWP		US- 2003/0179712	09-2003	Kobayashi et al.	
AWP		US- 2004-0034841-A1	02-19-2004	Reblewski	

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
AWP		EP 0,651,343 A1	05-03-1995	Quickturn Systems Inc.		

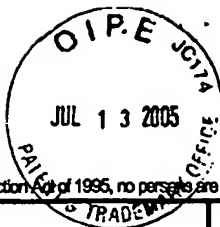
Examiner
SignatureALBERT W. PALADINI
PATENT EXAMINERDate
Considered

8-16-05

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PTO/SB/08b(08-03)
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Substitute for form 1449B/PTO		Complete if Known			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/003,951		
		Filing Date	10/30/2001		
		First Named Inventor	Frederic Reblewski		
		Art Unit	2125		
		Examiner Name	Paladini, Albert William		
Sheet	2	of	2	Attorney Docket Number	003921.00069

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
AW		Erhard et al., W. First Steps Towards a Reconfigurable Asynchronous System, IEEE International Workshop on Rapid System Prototyping, 1999, pgs. 28-31	
AW		Kocan et al., F. Concurrent D-Algorithm on Reconfigurable Hardware, IEEE/ACM International Conference on Computer-Aided Design, 1999, Digest of Technical Papers, pgs. 152-155	
AW		Clos, Charles, "A study of Non-Blocking Switching Networks," The Bell System Technical Journal, March 1953, pgs. 408-424	
AW		Bosl et al., B. Reconfigurable Pipelined 2-D Convolvers for Fast Digital Signal Processing, IEEE Transactions on Very Large Scale Integration (VLSI) Systems, vol. 7, Issue 3, Sept. 1999, pgs. 299-308	
AW		Ejrioui et al., A. Design Partitioning on Single-Chip Emulation Systems, 13 th International Conference on VLSI Design, 2000, pgs. 234-239	
AW		Jean, J.S.N., et al., "Dynamic Reconfiguration to Support Concurrent Applications," IEEE Transactions on Computers, June 1999, IEEE, USA, vol. 48, No. 6, pgs. 591-602	
AW		Snider, G., "The Teramac Compiler," Nov. 29, 1993, pgs. 1-51	
AW		Babb, J, et al., "Logic Emulation with Virtual Wires," IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, June 1997, pgs. 1-20	

Examiner Signature	ALBERT W. PALADINI <i>Albert W. Paladini</i>	Date Considered	8-16-05
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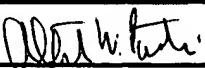
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Complete if Known	
				Application Number	10/003,951
				Filing Date	October 30, 2001
				First Named Inventor	Frederic Reblewski
				Group Art Unit	21822125
Sheet	2	of	2	Examiner Name	Not Yet Assigned
				Attorney Docket Number	109894-129745

OTHER ART (INCLUDING TITLE, AUTHOR, DATE, PERTINENT PAGES, ETC.)

Exam. Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
aur	3	XILINX, "The Programmable Gate Array Design Handbook," First Edition, 1986, pp. i - A-10

Examiner Signature	ALBERT W. PALADINI PATENT EXAMINER 	Date Considered	8-16-05
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First Named Inventor	Frederic Reblewski
Group Art Unit	2185 2125
Examiner Name	TBA
Attorney Docket Number	003921.00069

Sheet	1	of	1
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U.S. PATENT DOCUMENTS

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**Examiner
Signature**

ALBERT W. PALADIN

Date Considered

8-16-07

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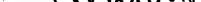
¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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